Electronic Acknowledgement Receipt				
EFS ID:	5903027			
Application Number:	10620045			
International Application Number:				
Confirmation Number:	7529			
Title of Invention:	Method and apparatus for automatic generation of multiple integrated circuit simulation configuration			
First Named Inventor/Applicant Name:	Rajarshi Bhattacharya			
Correspondence Address:	Ryan, Mason & Lewis, LLP - 90 Forest Avenue - Locust Valley NY 11560 US 5167597517 -			
Filer:	Joseph Bernard Ryan			
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1	1	1150-1145_AM3.pdf	767905	yes	9	
			033ce7e15079118db4422c918862f5eeb4f 2e9d			
	Multip	art Description/PDF files in	zip description			
Document I		scription	tion Start		End	
	Amendment/Argument after BPAI Decision Claims Amendment/Argument after BPAI Decision		1	1		
			2	6		
			7	9		
Warnings:						
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2	Request for Continued Examination (RCE)	1150-1145_RCE.pdf	156620 ca34b6320ubc08dddd6628518x25625ua3e9 c3db1	no	2	
Warnings:						
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3	Fee Worksheet (PTO-875)	fee-info.pdf	29683	no	2	
			c05bd1c27b4256cb7a02e4062270c6fd5ac 95de7			
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